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1 Characterizing the synchronization behavior of parallel programs
Helen Davis, John Hennessy
January 1988 **ACM SIGPLAN Notices , Proceedings of the ACM/SIGPLAN conference on Parallel programming: experience with applications, languages and systems**, Volume 23 Issue 9
Full text available: pdf(1.53 MB) Additional Information: full citation, abstract, references, citings, index terms

Contention for synchronization locks and delays waiting for synchronization events can substantially increase the running time of a parallel program. This makes it important to characterize the synchronization behavior of programs and to provide analysis tools to aid both the hardware and software designer in evaluating design alternatives. This paper describes a tracing facility that is incorporated into a synchronization package. This facility provides a portable means to accurately and e ...

2 Characterizing the caching and synchronization performance of a multiprocessor operating system
Josep Torrellas, Anoop Gupta, John Hennessy
September 1992 **ACM SIGPLAN Notices , Proceedings of the fifth international conference on Architectural support for programming languages and operating systems**, Volume 27 Issue 9
Full text available: pdf(1.52 MB) Additional Information: full citation, references, citings, index terms

3 Static synchronization beyond VLIW
H. Dietz, T. Schwederski, M. O'Keefe, A. Zaafrani
August 1989 **Proceedings of the 1989 ACM/IEEE conference on Supercomputing**
Full text available: pdf(1.13 MB) Additional Information: full citation, abstract, references, citings, index terms

A key advantage of SIMD (Single Instruction stream, Multiple Data stream) architectures is that synchronization is effected statically at compile-time, hence the execution-time cost of synchronization between "processes" is essentially zero. VLIW (Very Long Instruction Word) machines are successful in large part because they preserve this property while providing more flexibility in terms of what kinds of operations can be parallelized. In this paper, we propose a new kind of ar ...

4 Modeling issues in the design of embedded systems: An IDF-based trace transformation method for communication refinement